

42390.P9301

Patent**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

Manoj Khare et al.

Examiner: Tran, Denise

Serial No.: 09/643,380

Art Unit: 2186

Filing Date: August 21, 2000

For: A METHOD AND APPARATUS
FOR PIPELINING ORDERED
INPUT/OUTPUT TRANS-
ACTIONS TO COHERENT
MEMORY IN A DISTRIBUTED
MEMORY, CACHE COHERENT
MULTI-PROCESSOR SYSTEM

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Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE

Dear Sir:

In response to the Office Action mailed December 2, 2004, please enter the following amendments and consider the following remarks.

Amendments to the Claims are reflected in the listing of claims, which begins on page 2 of this paper.

Remarks/Arguments begin on page 9 of this paper.

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By: <u>Anne Collette</u> Anne Collette	Date: <u>March 2, 2005</u>

PTO/SB/21 (09-04)

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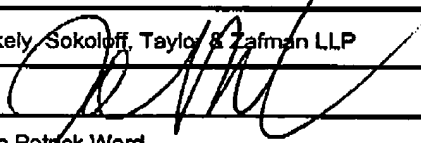
TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/643,380	
	Filing Date	August 21, 2000	
	First Named Inventor	Manoj Khare	
	Art Unit	2186	
	Examiner Name	Denise Tran	
Total Number of Pages in This Submission	11	Attorney Docket Number	42390.P9301

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Remarks		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	Blakely, Sokoloff, Taylor & Zafman LLP		
Signature			
Printed name	John Patrick Ward		
Date	March 2, 2005	Reg. No.	40,216

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Typed or printed name	Anne Collette	Date	March 2, 2005

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